

REPORT OF THE PROGRAM

The department of Electronics and Communication Engineering, Kavikulguru Institute of Technology and Science, Ramtek conducted the AICTE and ISTE sponsored six days online Induction program on "Recent trends in Embedded Systems (RTES)" in three Phases:

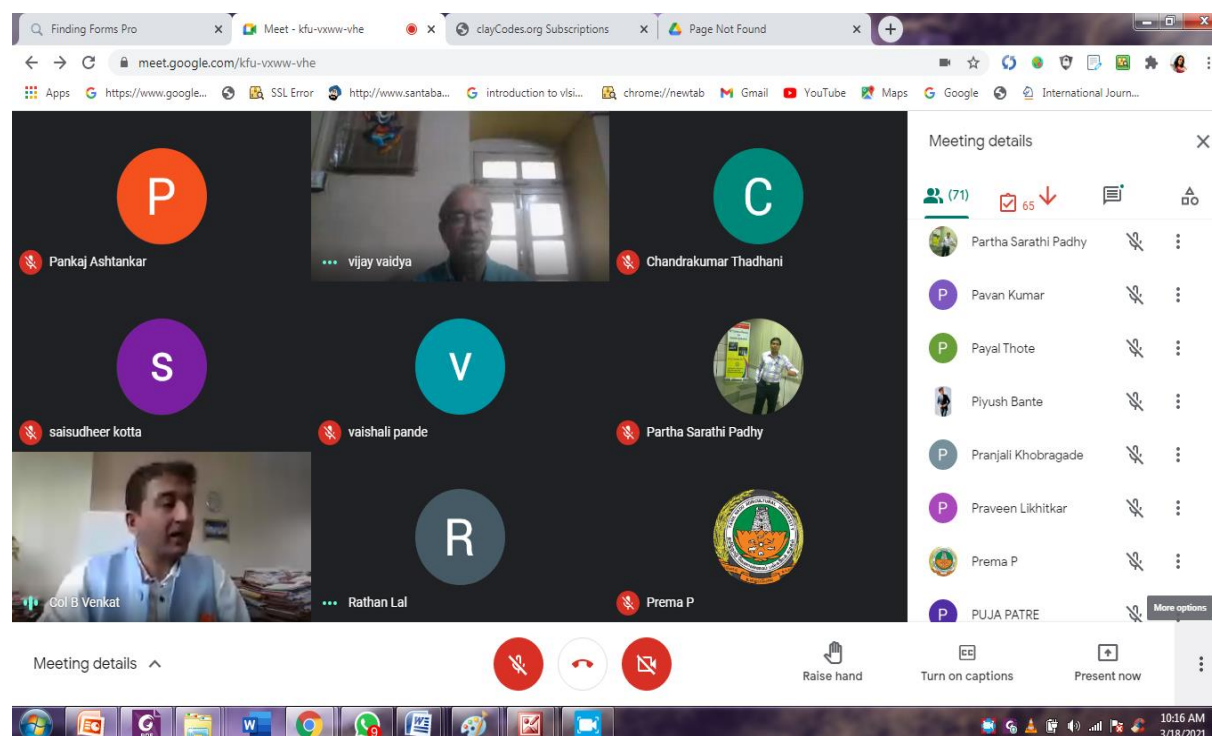
Phase1:18/3/2021 to 24/3/2021

Phase2: 27/5/2021 to 2/6/2021

Phase3: 9/6/2021 to 15/6/2021

PHASE 1:

The inauguration program for the first phase was conducted successfully on 18/3/2021 at 10.00 AM. The program was blessed with the presence of Prof. Vijay D. Vaidya , Executive Secretary ISTE, New Delhi as Chief Guest of the function and Col. B. Venkat , Director ,Faculty Development cell, AICTE ,New Delhi, as a guest of honor. Dr. B. Ram Rathan Lal , Principal and Dr. Pankaj S. Ashtankar , HOD ECE & Coordinator were also present . The chief guest said that such programs to be encouraged in every corner of the country. The inauguration program was conducted successfully with the efforts of the entire RTES-2021 team. Fifty-two participants from various fields in engineering domain across the country have attended the program.





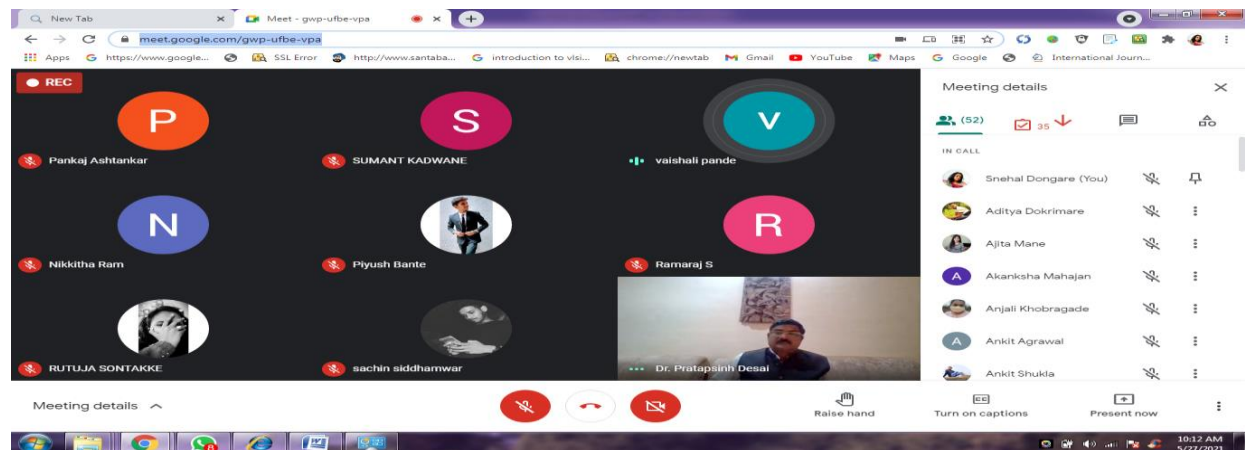
PHASE 2:

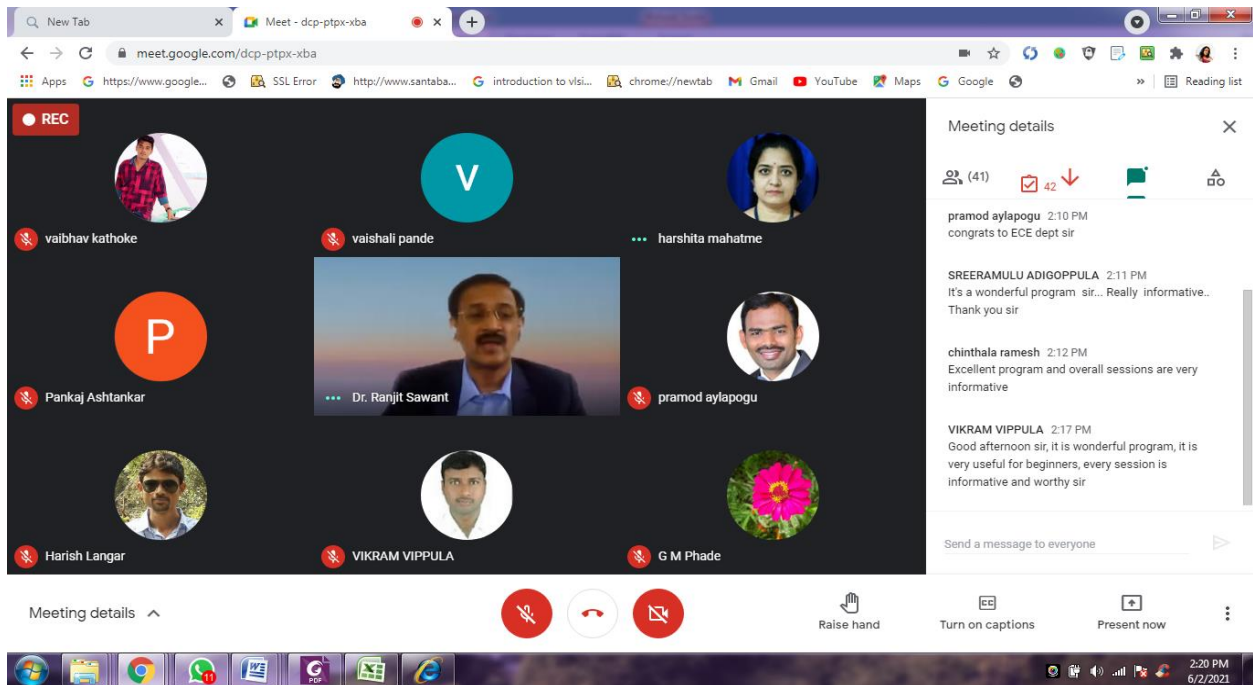
The second phase of the program was conducted from 27/5/2021 to 2/6/2021.

“Embedded Systems, influencing the Electronics industry”

The first phase was completed successfully. The inauguration program for the second phase was conducted successfully on 27/5/2021 at 10.00 AM. The program was blessed with the presence of Dr. Pratapsinh Kakasaheb Desai, President, ISTE, New Delhi as Chief Guest of the function.

The valedictory function for the second phase was conducted on 2/6/2021 at 2:00 PM. The program was graced by the presence of Dr. Ranjeet Krishnarao Sawant, Executive Council Member ISTE, New Delhi as chief guest, and Dr. B.Ram Rathan Lal, Principal and the Program Coordinator, Dr. Pankaj S. Ashtankar, department faculty members were present for the program. The inauguration and the valedictory function were conducted successfully with the efforts of the entire RTES-2021 team. About 45 participants from various fields in the engineering domain across the country attended the program





PHASE 3:

The third phase of the program was conducted from 9/6/2021 to 15/6/2021.

“Embedded technologies are evolving — performing faster, becoming more compact, and costing less”

The inauguration program for the third phase was conducted successfully on 15/6/2021 at 10.00 AM. The program was blessed with the presence of Prof. Vijay D. Vaidya, Executive Secretary, ISTE, New Delhi as Chief Guest of the function.

The valedictory function for the third phase was conducted on 15/6/2021 at 2:00 PM. The program was graced by the presence of Col. B. Venkat, Director, Faculty Development Cell, AICTE, New Delhi as chief guest. Dr. B. Ram Rathan Lal , Principal and the Coordinator of the program, Dr. Pankaj S. Ashtankar, and faculty of the department were present for the program. The Chief Guest in his talk said that ' With growth and advancements in the field of electronics, wireless communications, networking, and robotics, devices around us communicate in more ways than ever imagined' The inauguration and the valedictory functions were conducted successfully with the efforts of the entire RTES-2021 team. About 44 participants from various fields in the engineering domain across the country attended the program.

